

FIG. 1

	HIGH BIT	LOW BIT
$V_t \uparrow$	0	0 = L3
	0	1 = L2
	1	0 = L1
	1	1 = L0

FIG. 2

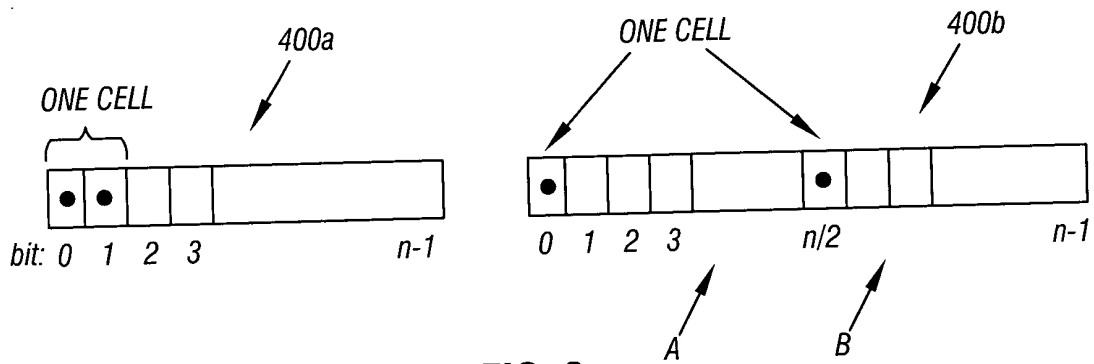


FIG. 3

bit: 0	1	n/2	n-1	510	540	CODEWORD
HIGH BITS (520)		LOW BITS (530)		1	P	A
				1	P	B
				0		C
				1	P	D
				0		E
				1	P	F
				1	P	G
				1	P	H

500

FIG. 4

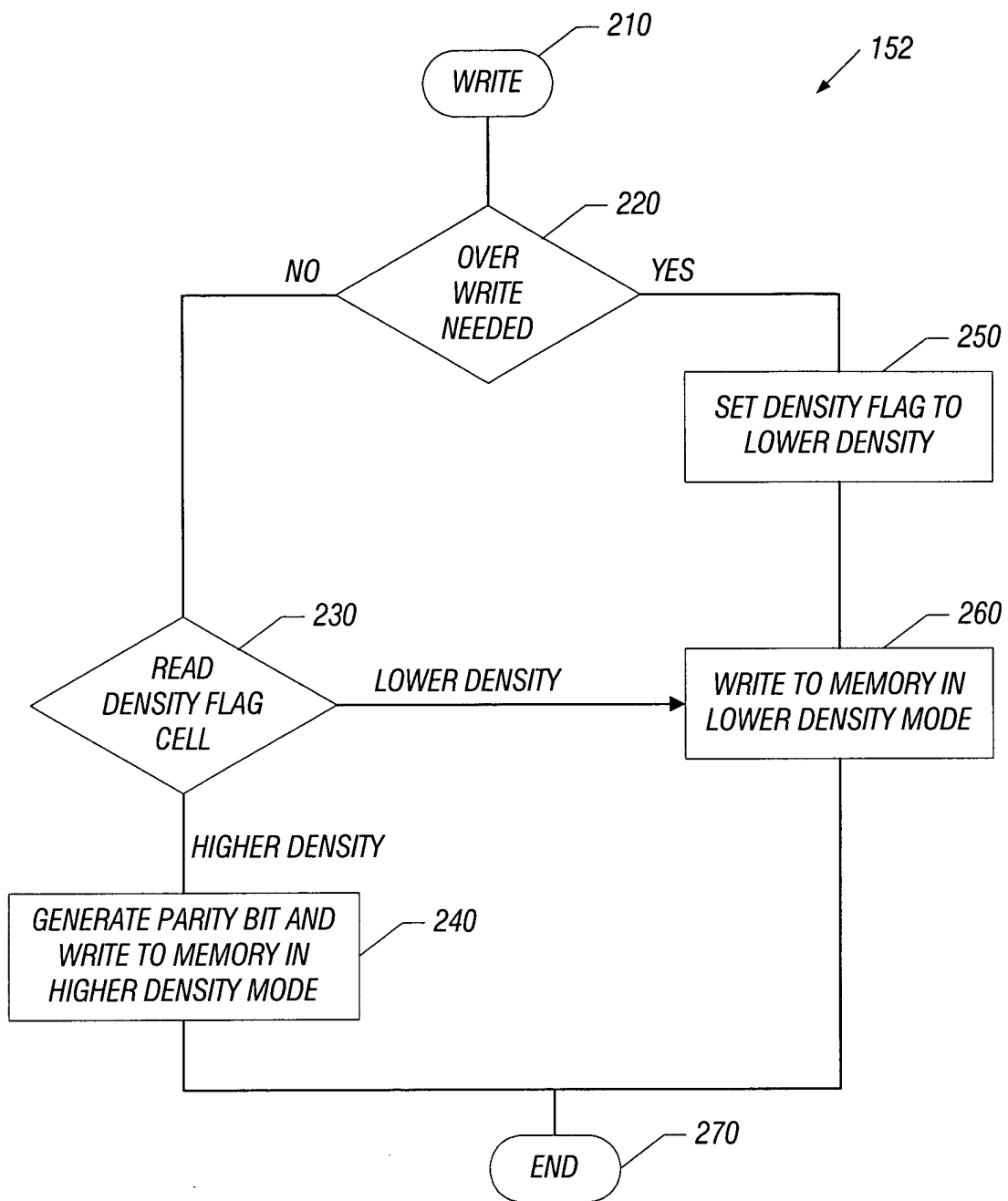


FIG. 5